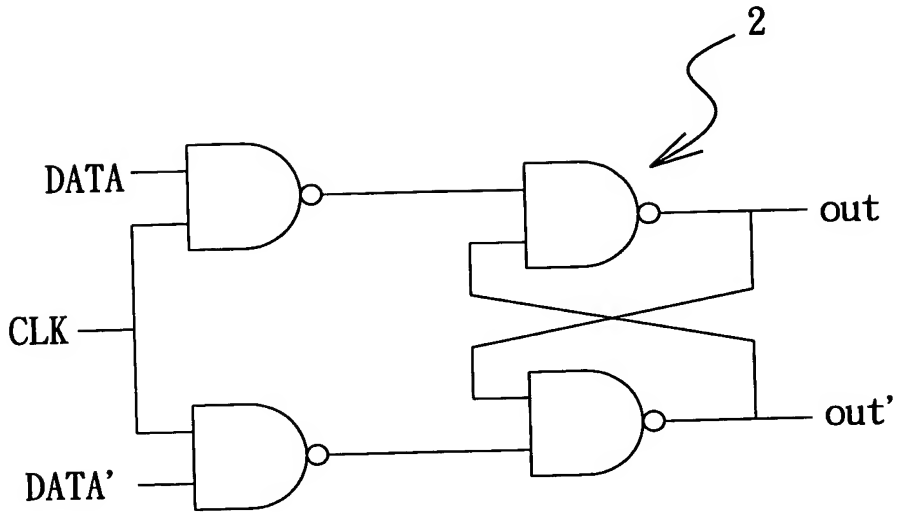
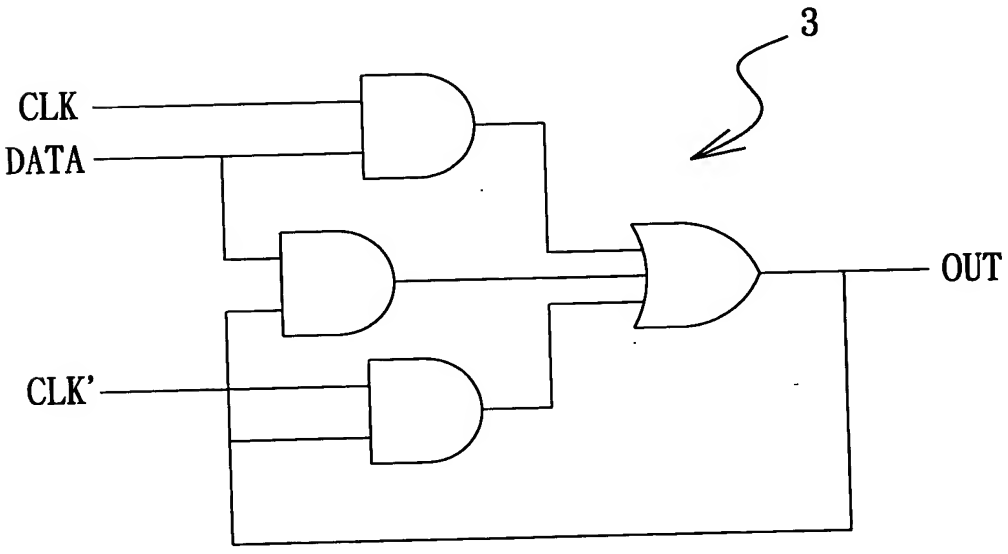


F I G. 1
PRIOR ART



F I G. 2
PRIOR ART

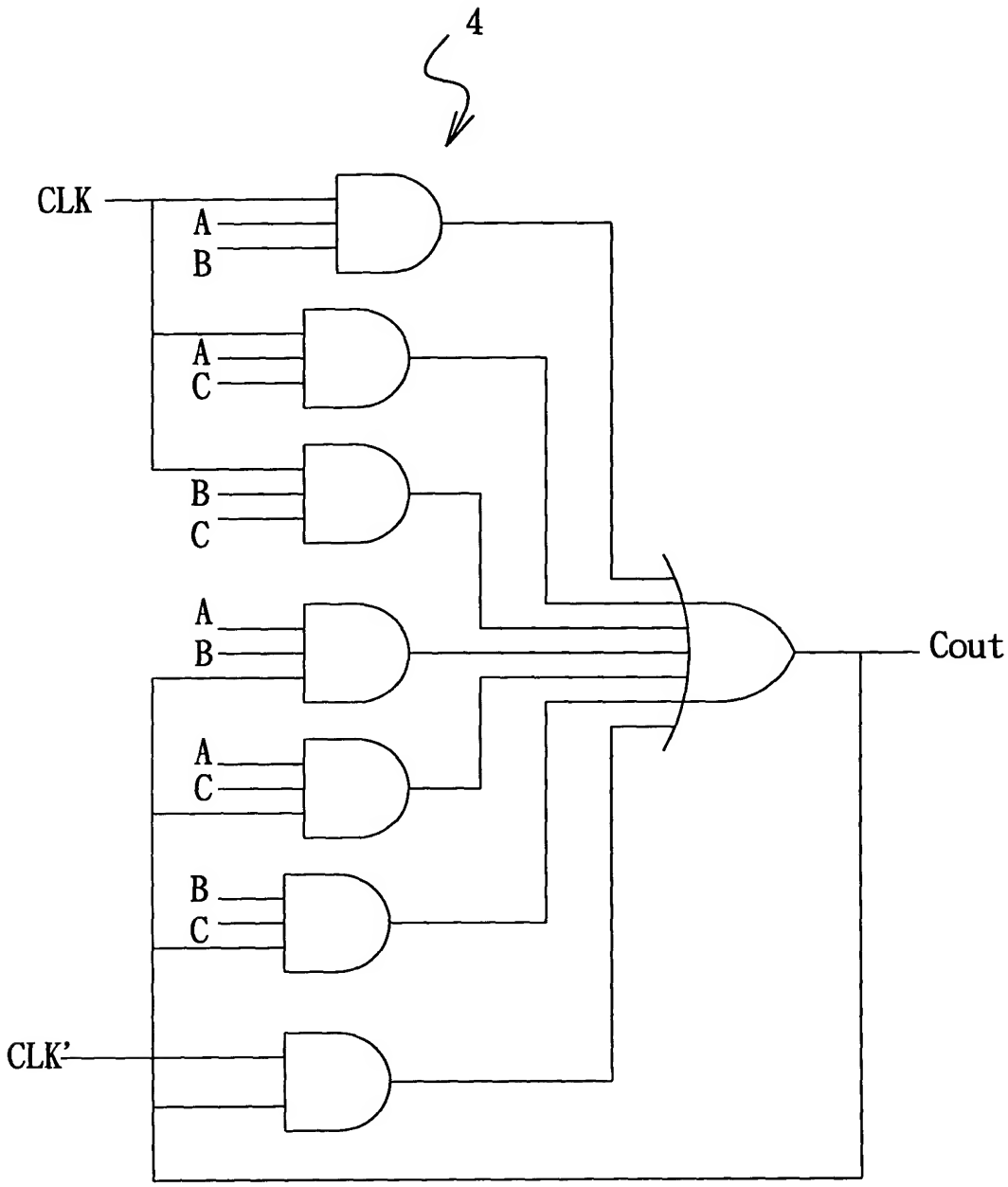
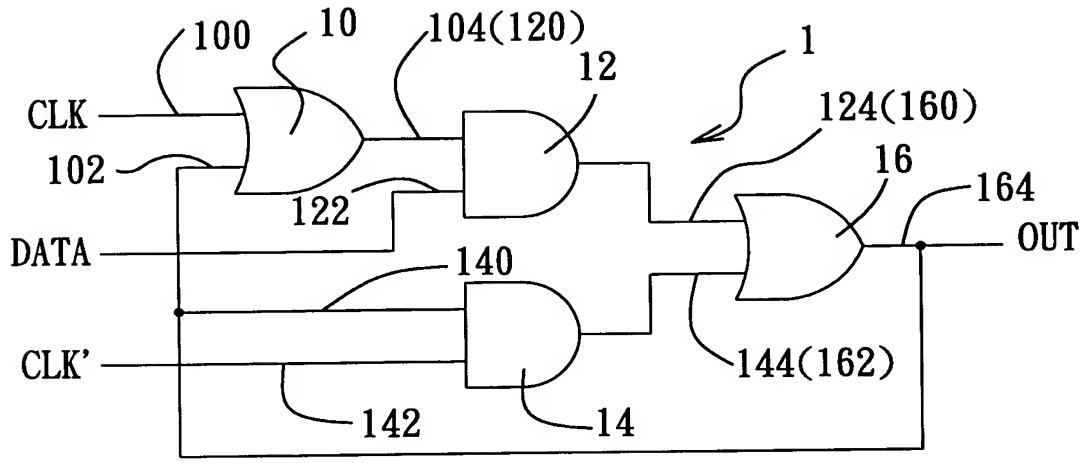
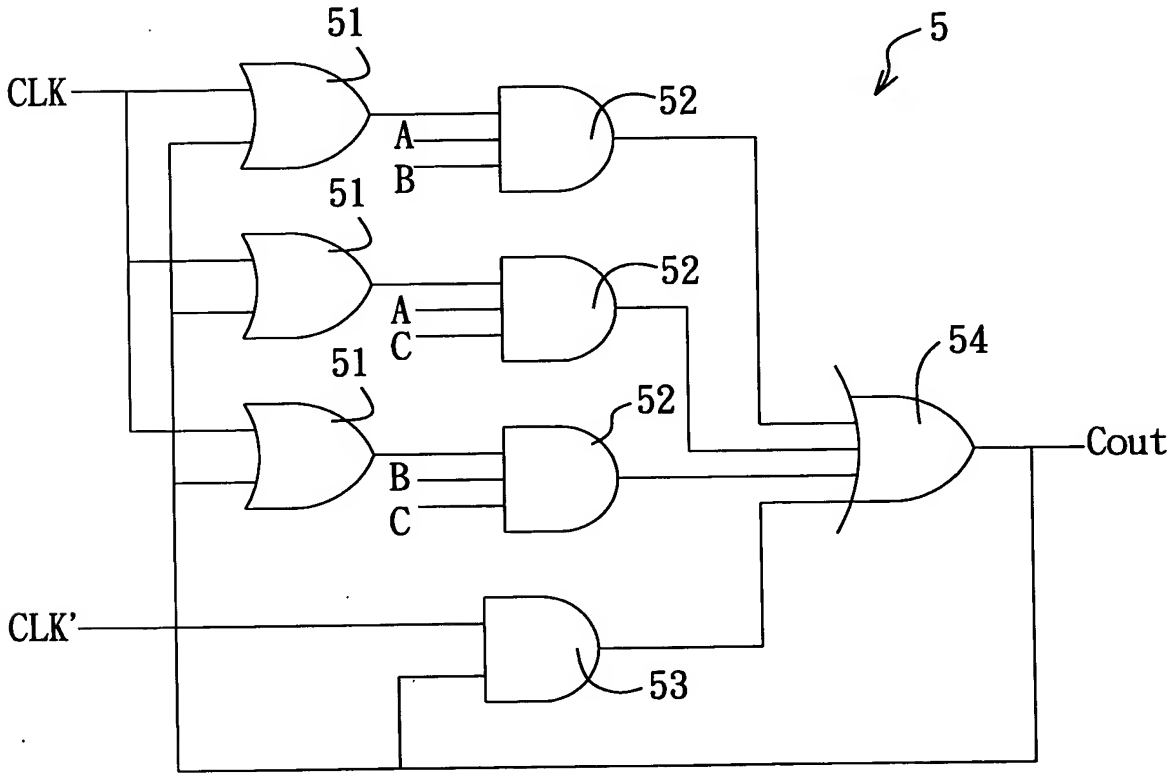


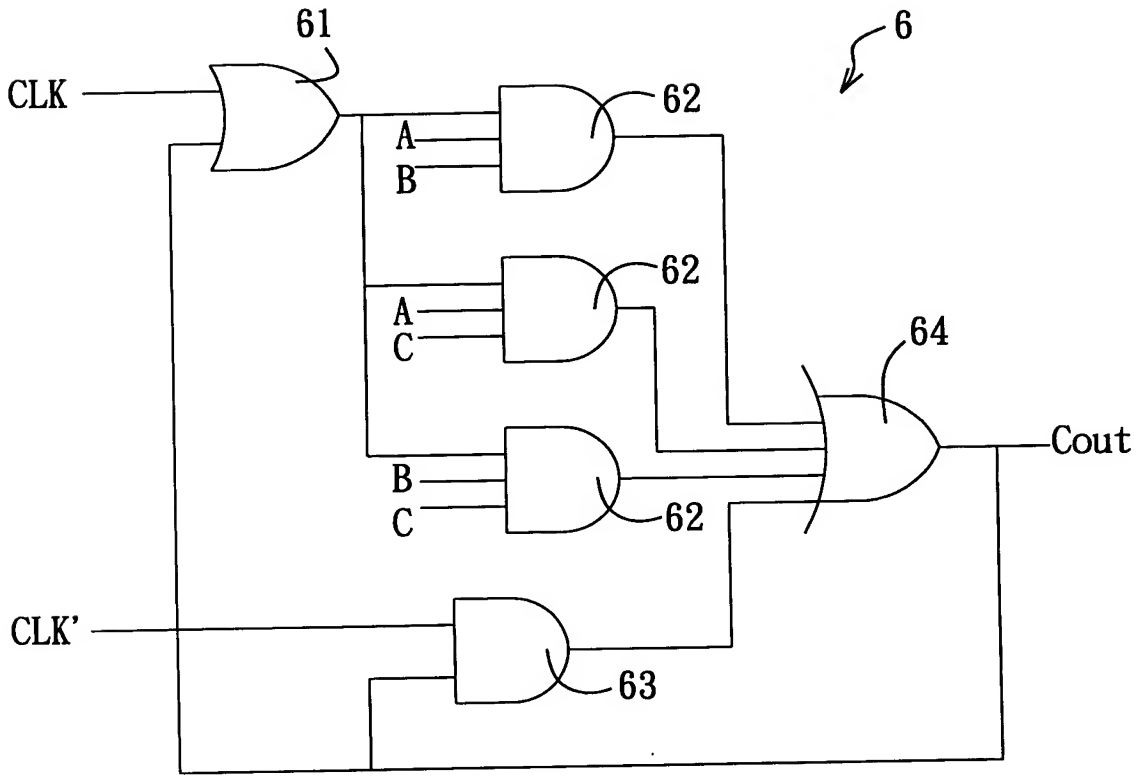
FIG. 3
PRIOR ART


100
10
104(120)
12
1
CLK
124(160)
102
16
164
122
OUT
DATA
140
CLK'
144(162)
142
14

F I G. 4


51
5
CLK
52
A
B
51
52
A
C
54
51
52
Cout
B
C
CLK'
53

F I G. 5



F I G. 6

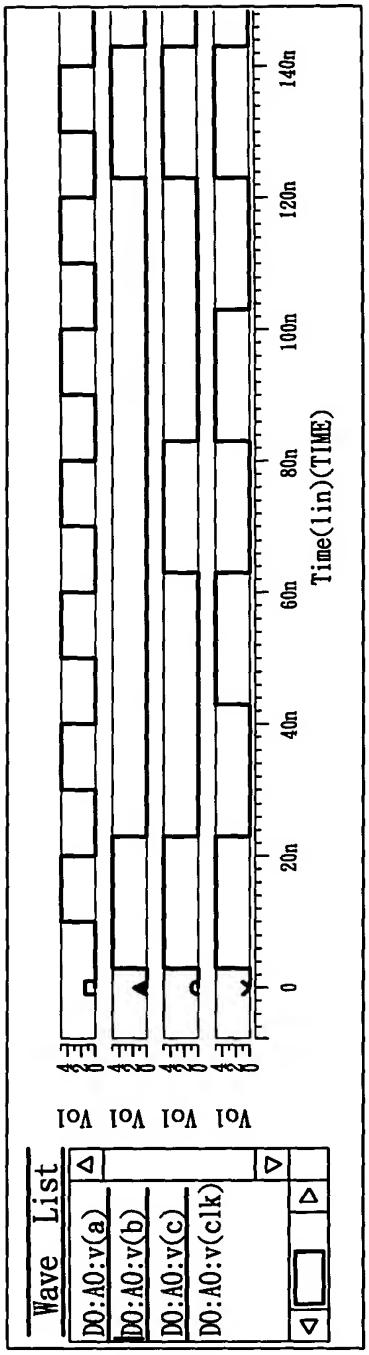


FIG. 7

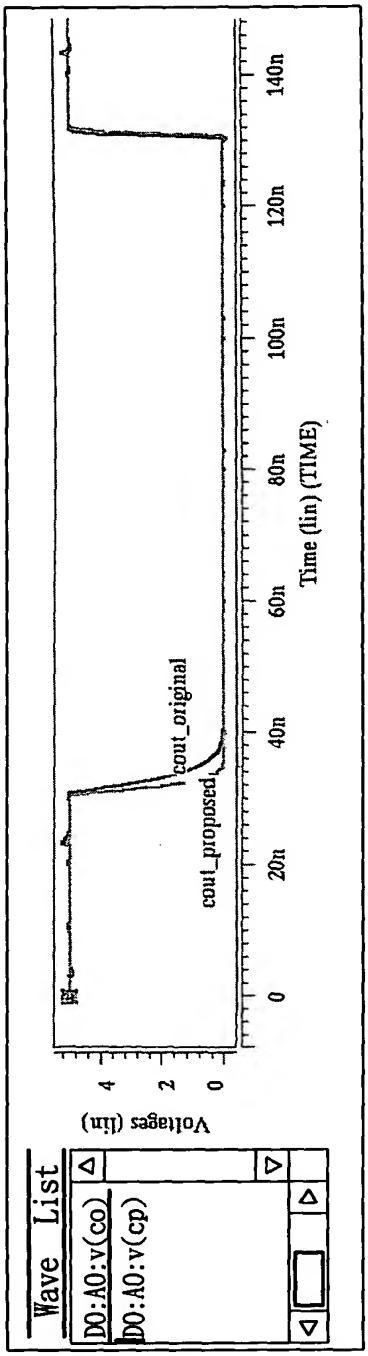


FIG. 8

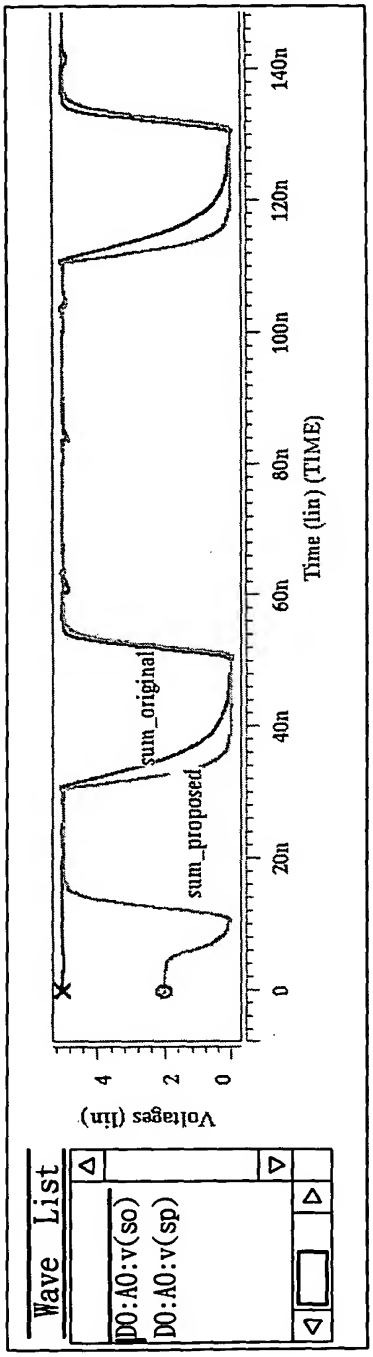


FIG. 9

	Present invention	Conventional D-latch	Conventional Earle latch	Jamb latch	transmission -gate latch	Tri-state buffer latch	Modified Svensson latch
Unit area	14	18	18	6	10	10	7
Total area	36	72	54	36	48	48	39
Delay (ns)	0.274	0.284	0.313	0.262	0.255	0.396	0.209
Power (mW)	0.144	0.316	0.164	0.328	0.363	0.299	0.148

F I G. 10

Title: FEEDBACK LATCH CIRCUIT AND METHOD THEREFOR
Inventors: Tsin-Yuan Chang; Hao-Yung Lo; and Shao-Sheng Yang
Docket No.: SIPT121661